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James J. Bode (Depositor's name)
[Signature] (Signature)
March 31, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/840,386 04/23/2001 Yoshihisa Matsubara NEKA 18.612 2510

TITLE OF INVENTION: METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE, AND PROCESSING SYSTEM AND SEMICONDUCTOR DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO \$1400 \$300 \$1700 04/13/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
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VINH, LAN 1765 438-692000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

NEC Electronics Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kawasaki, Japan

01 FC:1501 1400.00 DA
 02 FC:1504 300.00 DA
 03 FC:8001 15.00 DA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entry Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant, a registered attorney or agent, or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature

Typed or printed name

Brian E. Hennessy

Date

Registration No.

March 31, 2006

51,271

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T-836 P.004/005 F-848

Attorney Docket No.: NEKA 18.612 (100806-00199)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : **Yoshihisa Matsubara, et al.**
Title : **METHOD FOR MANUFACTURING ...**
Serial No. : **09/840,386**
Confirmation No. : **2510**
Filed : **04/23/2001**
Examiner : **Lan Vinh**
Group Art Unit : **1765**

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P.O. Box 1450
Alexandria, VA 22313-1450

Comments on Statement of Reasons for Allowance

Sir:

The referenced application was allowed on January 13, 2006. The issue fee due on April 13, 2006 is being paid concurrently herewith.

Applicant hereby acknowledges the Examiner's Reasons for Allowance. Applicant respectfully notes that there may be additional reasons for allowance that have not been specifically cited, and which may apply to various of the allowed claims, in addition to or instead of the cited reasons. Applicant respectfully suggests that notwithstanding the Examiner's Reasons for Allowance, it is believed that each of the allowed claims is patentable in its own

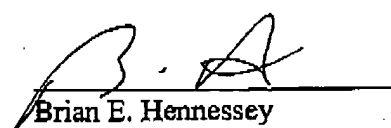
41292226.01

right and/or for other reasons raised during the prosecution and/or explained in the specification of this application.

To the extent that any statements regarding patentability of any claims allowed by the Examiner made by the Applicant or the Examiner in any document filed in this application are inconsistent with or not included in the Examiner's Reasons for Allowance, they are incorporated by reference herein.

Any fee due with this paper may be charged on Deposit Account No. 50-1290.

Respectfully submitted,


Brian E. Hennessey
Reg. No.51,271

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T-834 P.001/005 F-848

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Date	Client/Matter Number
March 31 2006	100806-00199
From	Attorney Number
James J. Dade	03290
Phone	Fax
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Total number of pages, including cover letter: 5
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Comments:

U.S. Patent Application No.: 09/840,386

METHOD FOR MANUFACTURING SEMICONDUCTOR

Our Reference No.: NEKA 18.612 (100806-00199)

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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